

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :Kazuo GOUDA et al.

Group Art Unit : 1792

Appl. No. : 10/598,372

Examiner : Kin Chan CHEN

Filed : August 25, 2006

Confirmation No. : 3256

For : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

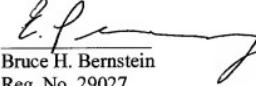
Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop Issue Fee  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir :

In response to the Notice of Allowability, mailed by the U.S. Patent and Trademark Office on June 4, 2009, and to the Statement of Reasons for Allowance attached thereto, Applicants wish to clarify the record with respect to the basis for the patentability of claims in the present application. In this regard, while Applicants do not disagree with the Examiner's indication of allowability, Applicants submit that each of the claims in the present application recites a combination of features, and that the basis for patentability of each of these claims is based on the combination of features recited therein.

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully Submitted,  
Kazuo GOUDA et al.

  
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August 3, 2009  
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